

1           1.    A method comprising:  
2                handling the system-wide state of a wireless  
3 device through the host controller interface firmware; and  
4                handling the state of each link with the device  
5 through the link manager firmware.

1           2.    The method of claim 1 including initiating  
2 communications with connection and link management.

1           3.    The method of claim 1 including establishing a  
2 connection between the device and an end point.

1           4.    The method of claim 3 including handling baseband  
2 handshaking through the host controller interface firmware.

1           5.    The method of claim 3 including handling logical  
2 link connection through the link manager firmware.

1           6.    An article comprising a medium storing  
2 instructions that enable a processor-based system to:  
3                handle the system-wide state of a wireless device  
4 through the host controller interface firmware; and  
5                handle the state of each link with the device  
6 through the link manager firmware.

1           7.    The article of claim 6 further storing  
2 instructions that enable the processor-based system to  
3 initiate communications with connection and link  
4 management.

1           8.    The article of claim 6 further storing  
2 instructions that enable the processor-based system to  
3 establish a connection between the device and an end point.

1           9.    The article of claim 8 further storing  
2 instructions that enable the processor-based system to  
3 handle baseband handshaking through the host controller  
4 interface firmware.

1           10.   The article of claim 8 further storing  
2 instructions that enable the processor-based system to  
3 handle logical link connection through the link manager  
4 firmware.

1           11.   A wireless system comprising:  
2                a processor; and  
3                a storage coupled to said processor storing  
4 instructions that enable the processor to:  
5                handle the system-wide state of the system  
6 through the host controller interface firmware; and

7           handle the state of each link with the system  
8   through the link manager firmware.

1           12. The system of claim 11 wherein said storage  
2   stores instructions that enable the processor to initiate  
3   communications with connection and link management.

1           13. The system of claim 11 wherein said storage  
2   stores instructions that enable the processor to establish  
3   a connection between the system and a remote end point.

1           14. The system of claim 13 wherein said storage  
2   stores instructions that enable the processor to handle  
3   baseband handshaking through the host controller interface  
4   firmware.

1           15. The system of claim 13 wherein said storage  
2   stores instructions that enable the processor to handle  
3   logical link connection through the link manager firmware.